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Figure 1: Development Flow

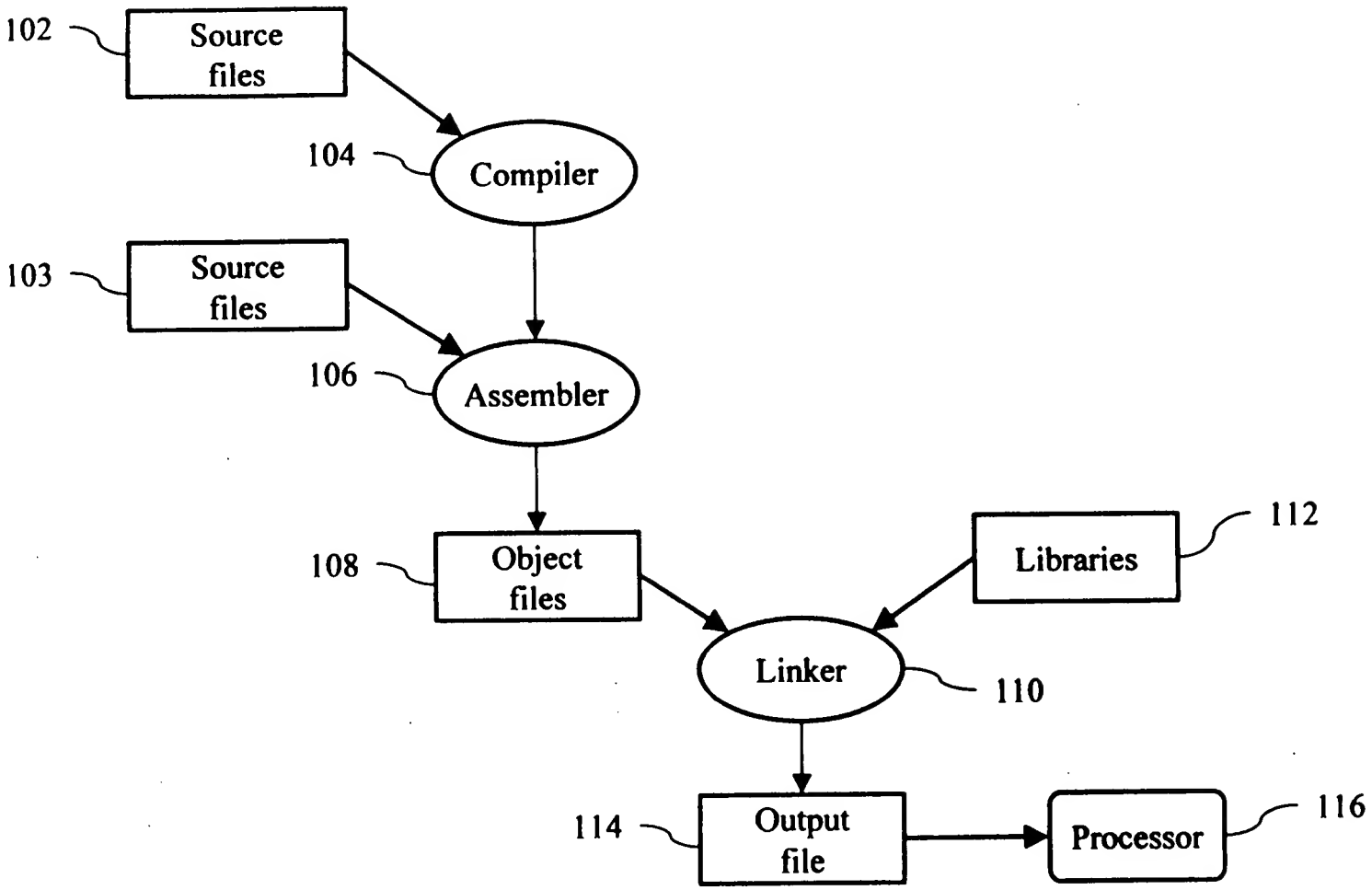


Figure 2A: Typical Linker Apparatus

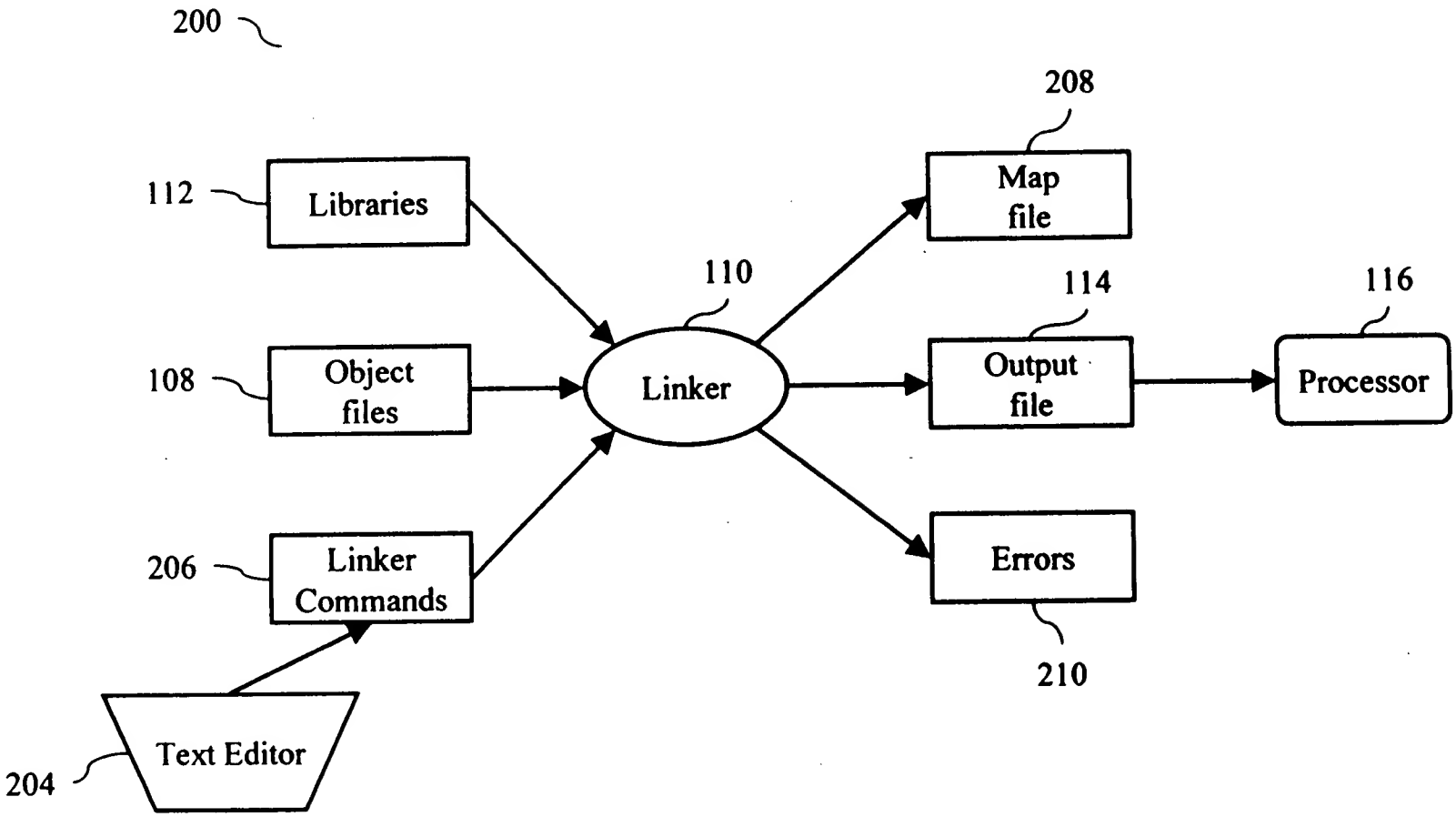


Figure 2B: Typical Linker Method

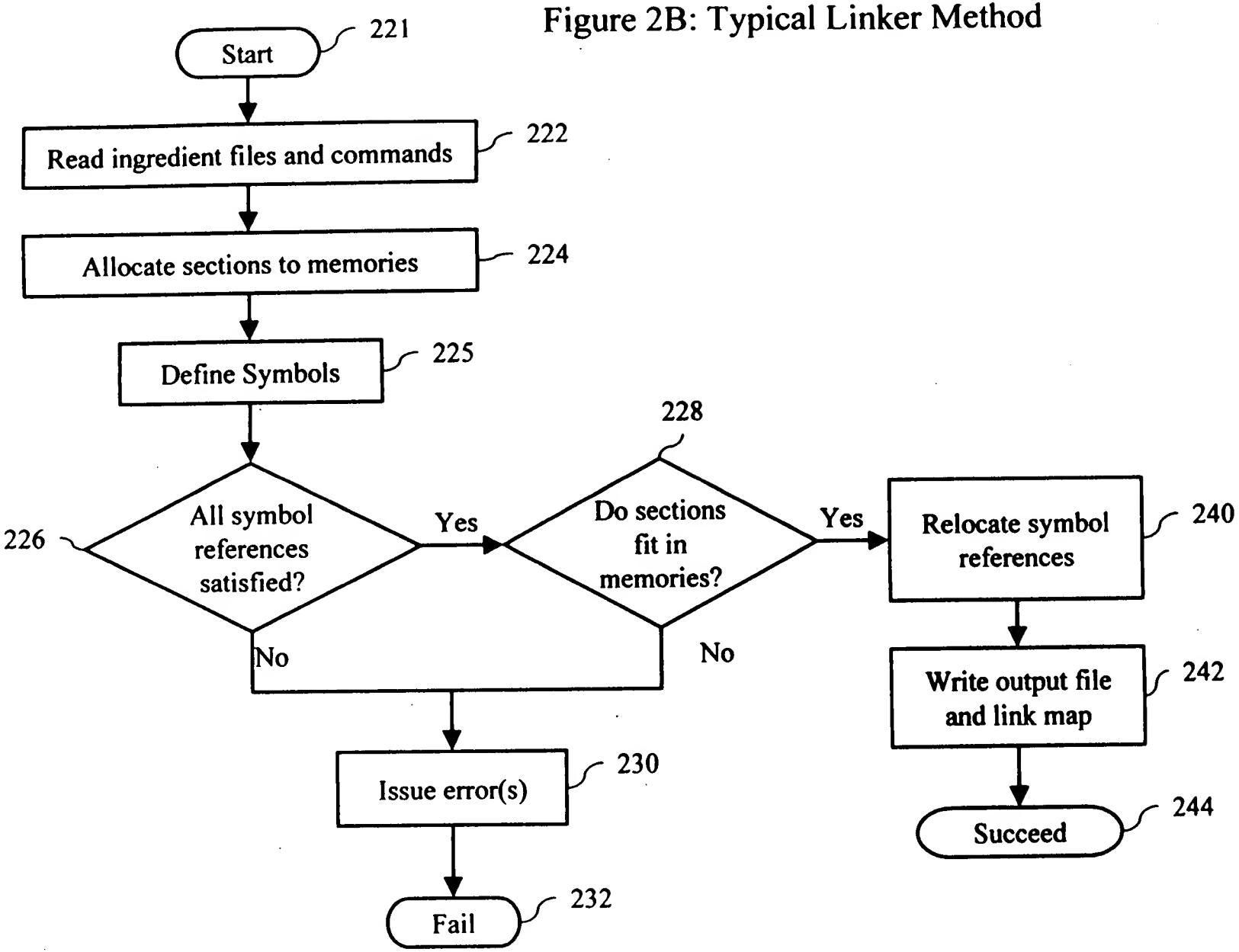


Figure 3: Linker Allocation and Output

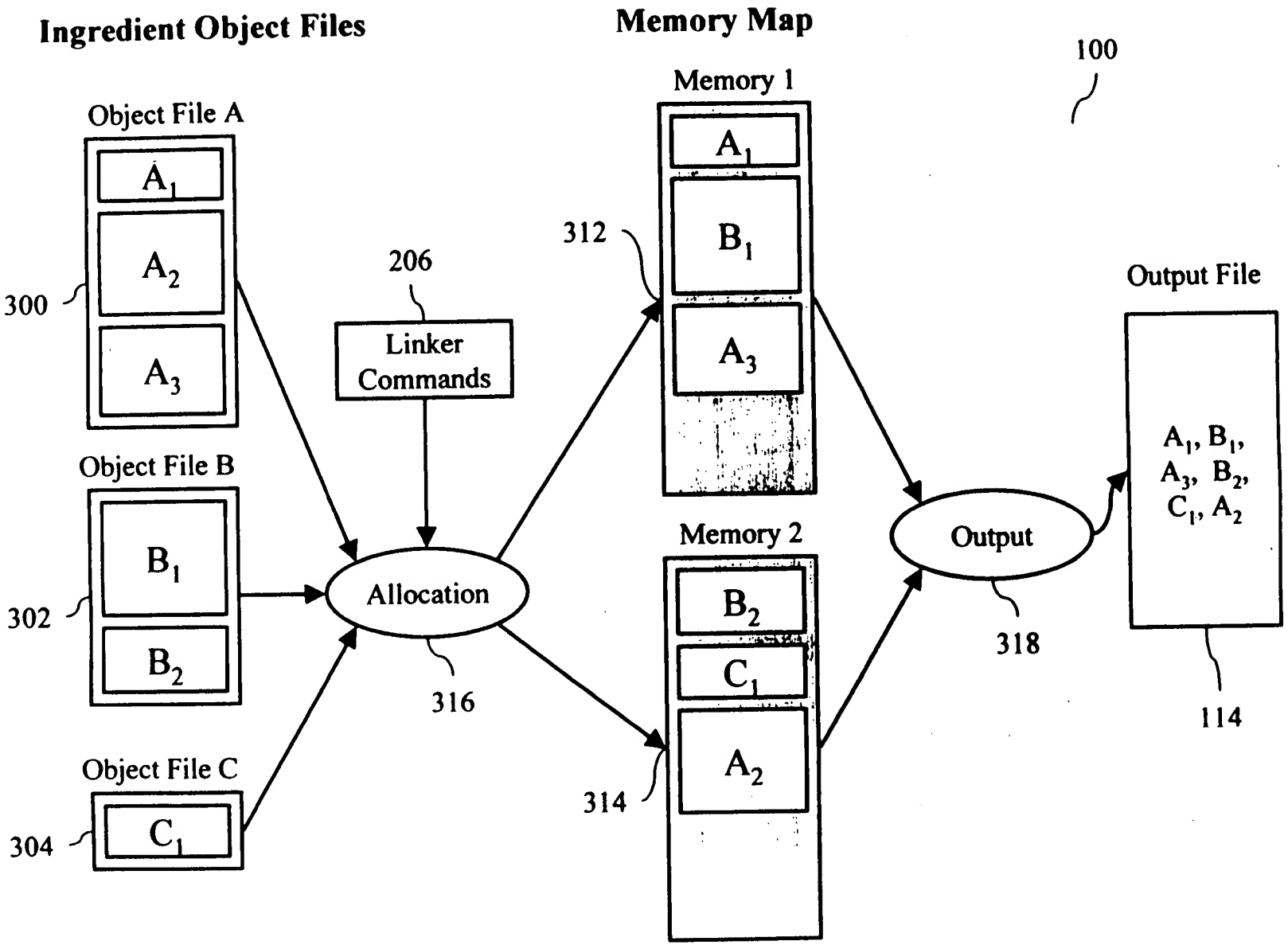
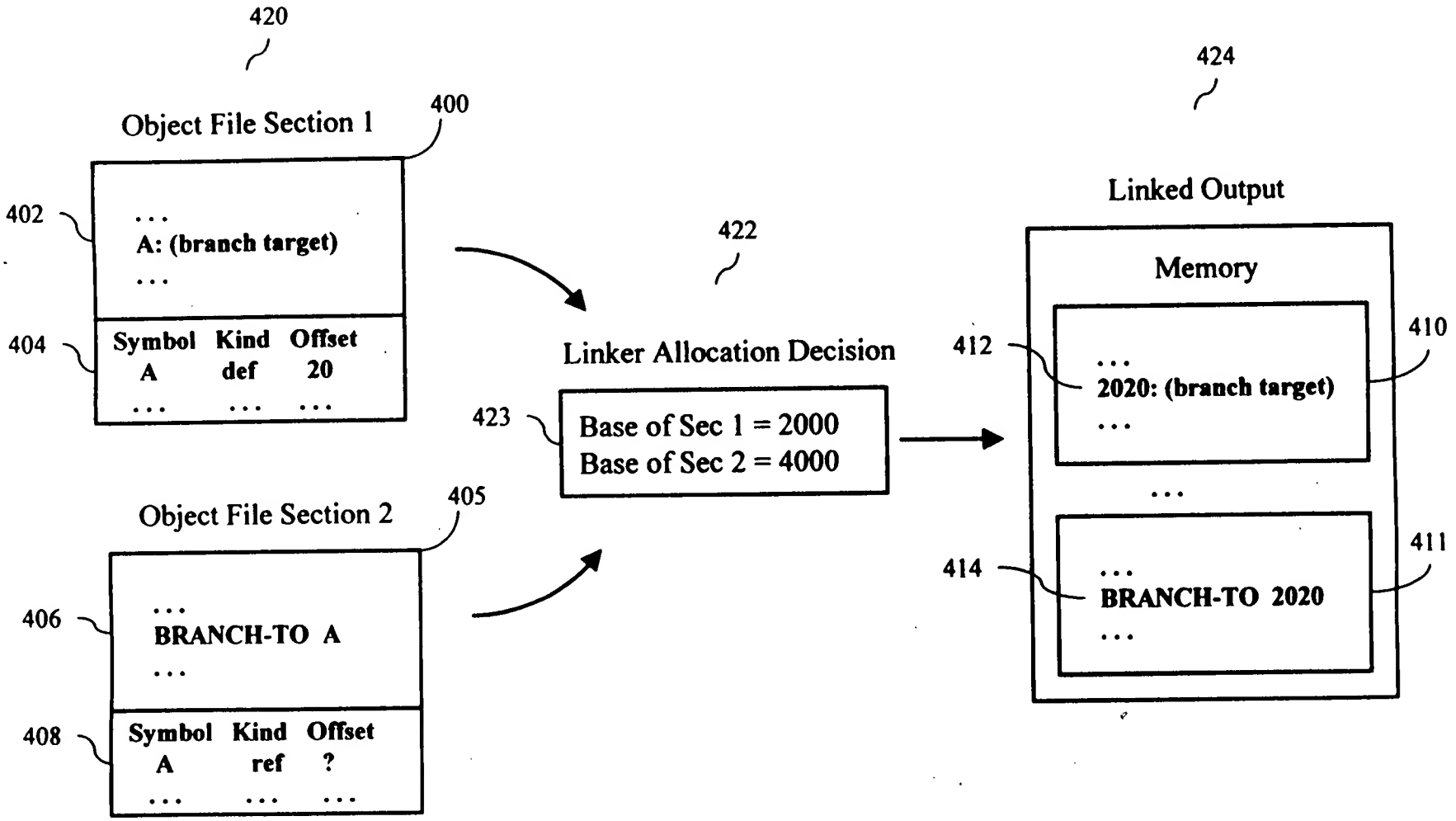


Figure 4: Linker Symbol Resolution



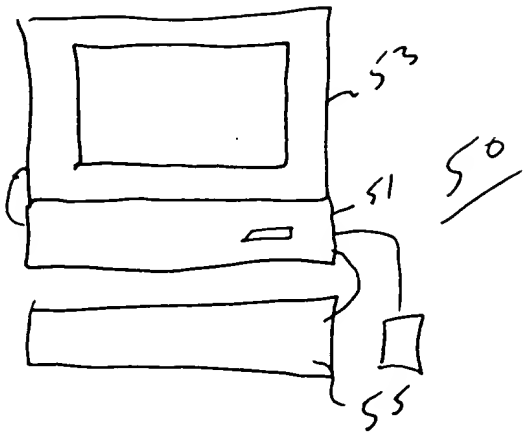


Fig. 5

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Figure 6: Original code as produced by compiler or assembler.

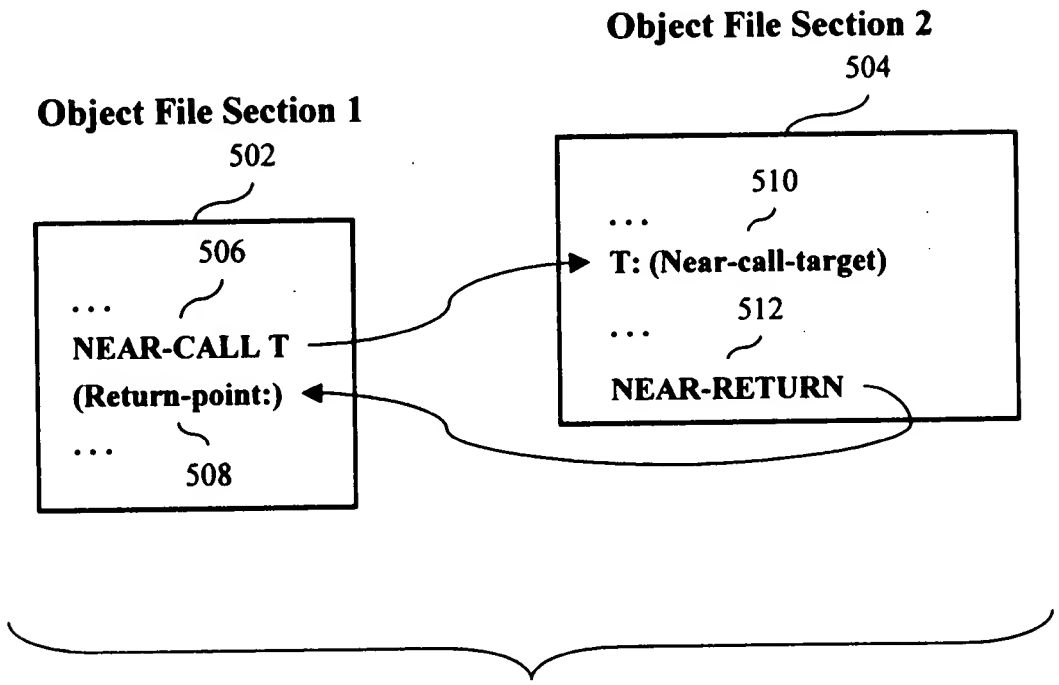

Figure 7: Near call after fixup with single trampoline.

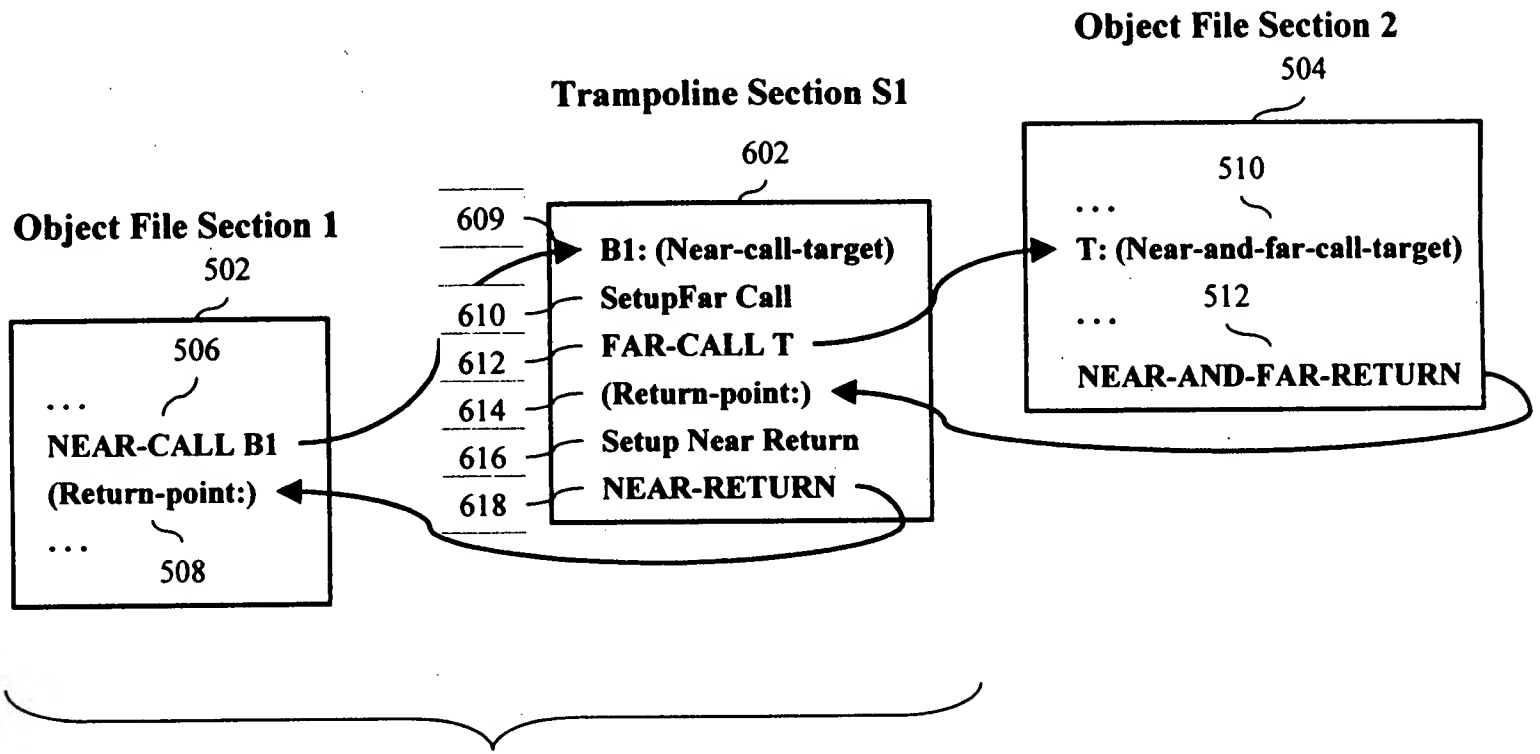

Figure 8 : Call after fixup with two trampolines.

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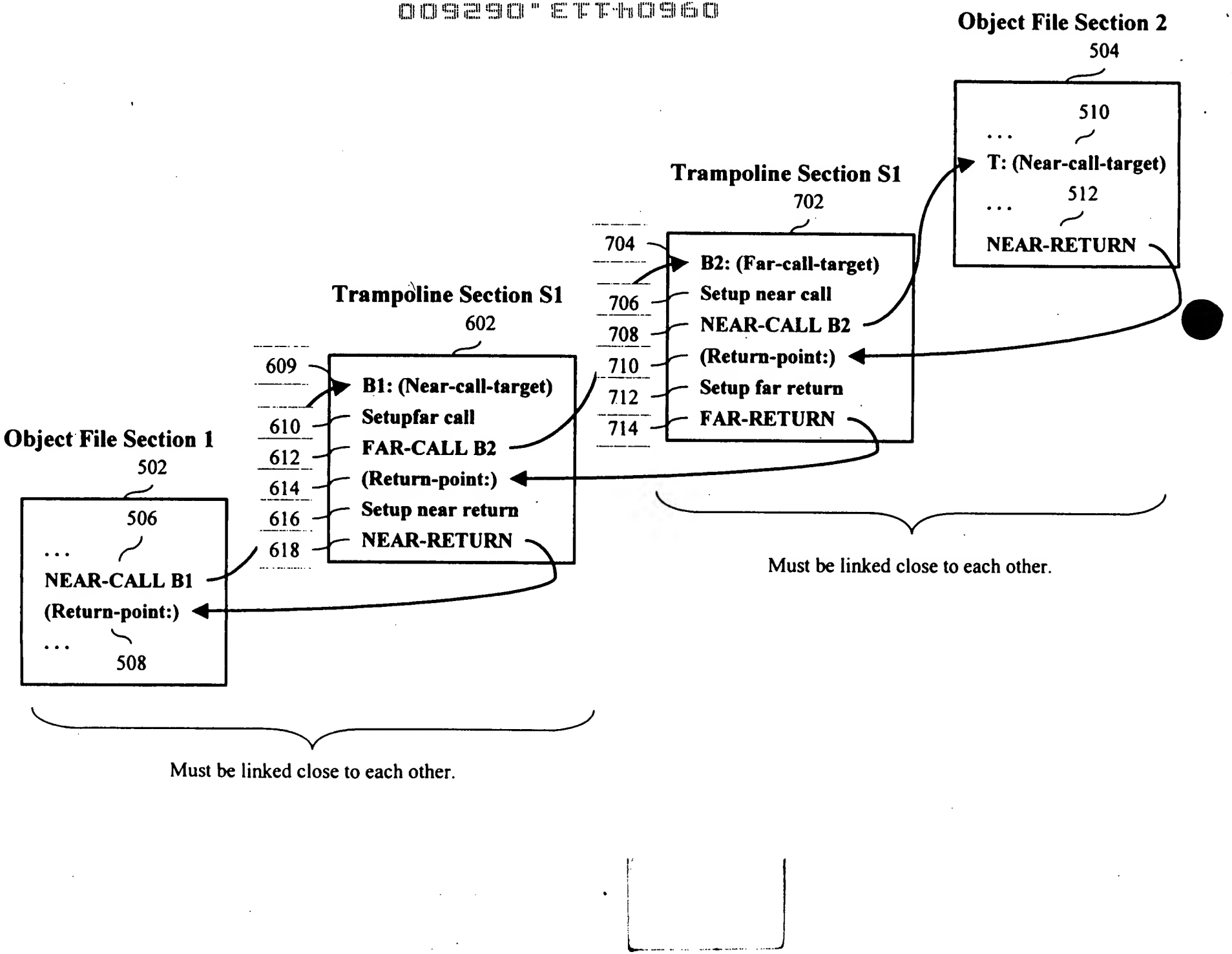

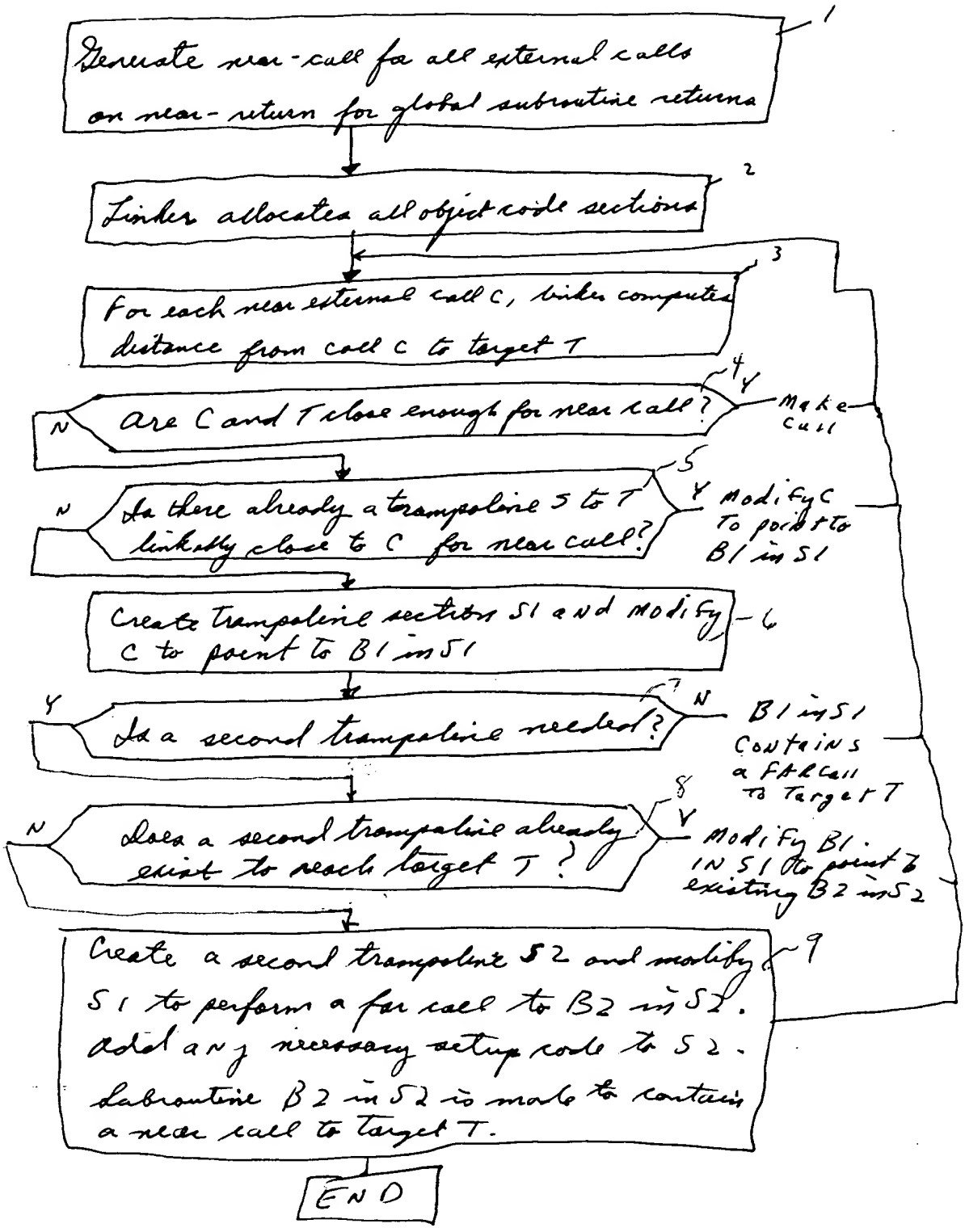

Fig. 9

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TRAMPOLINES

Fig. 10 (10)

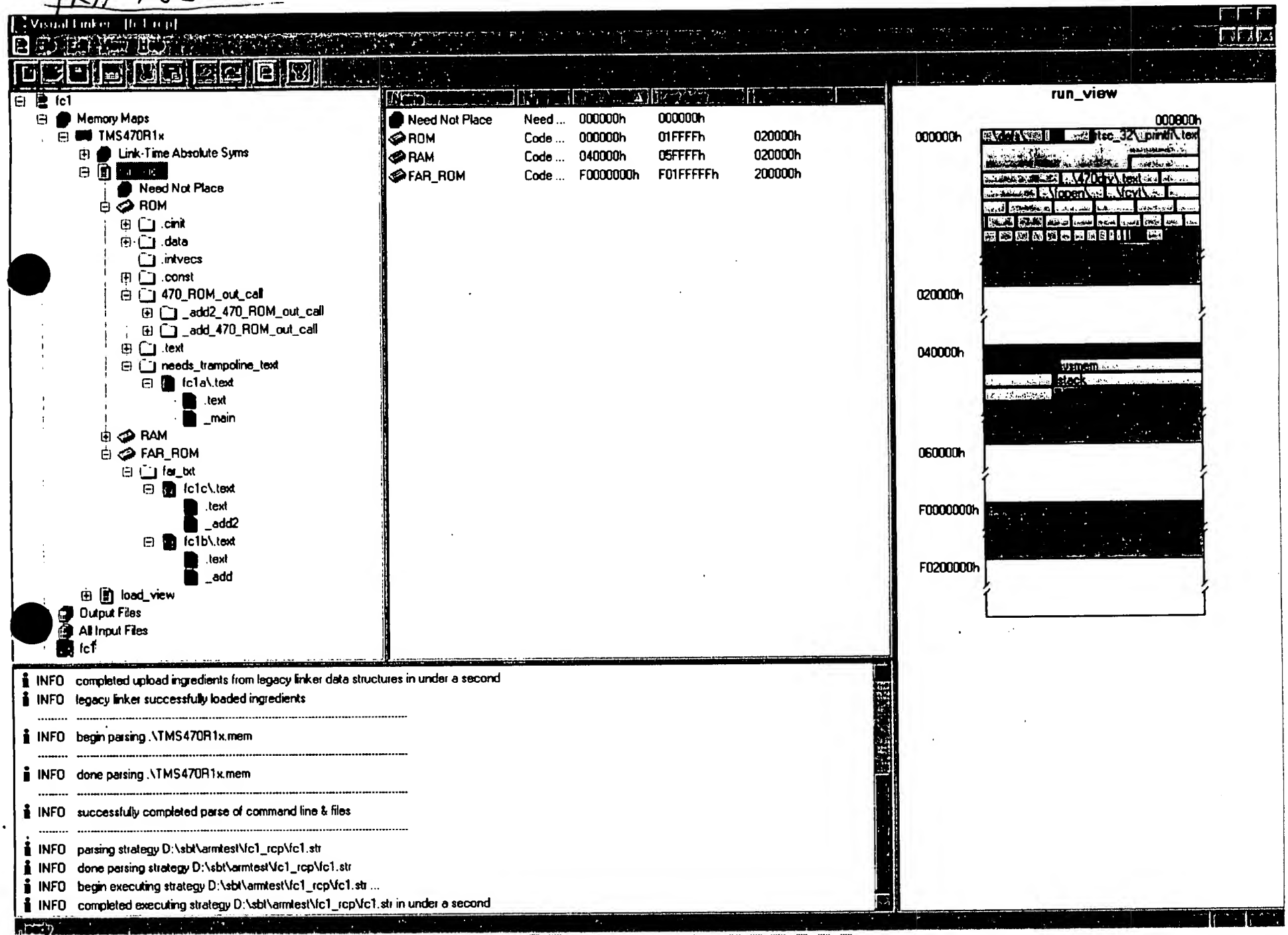

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Fig. 11

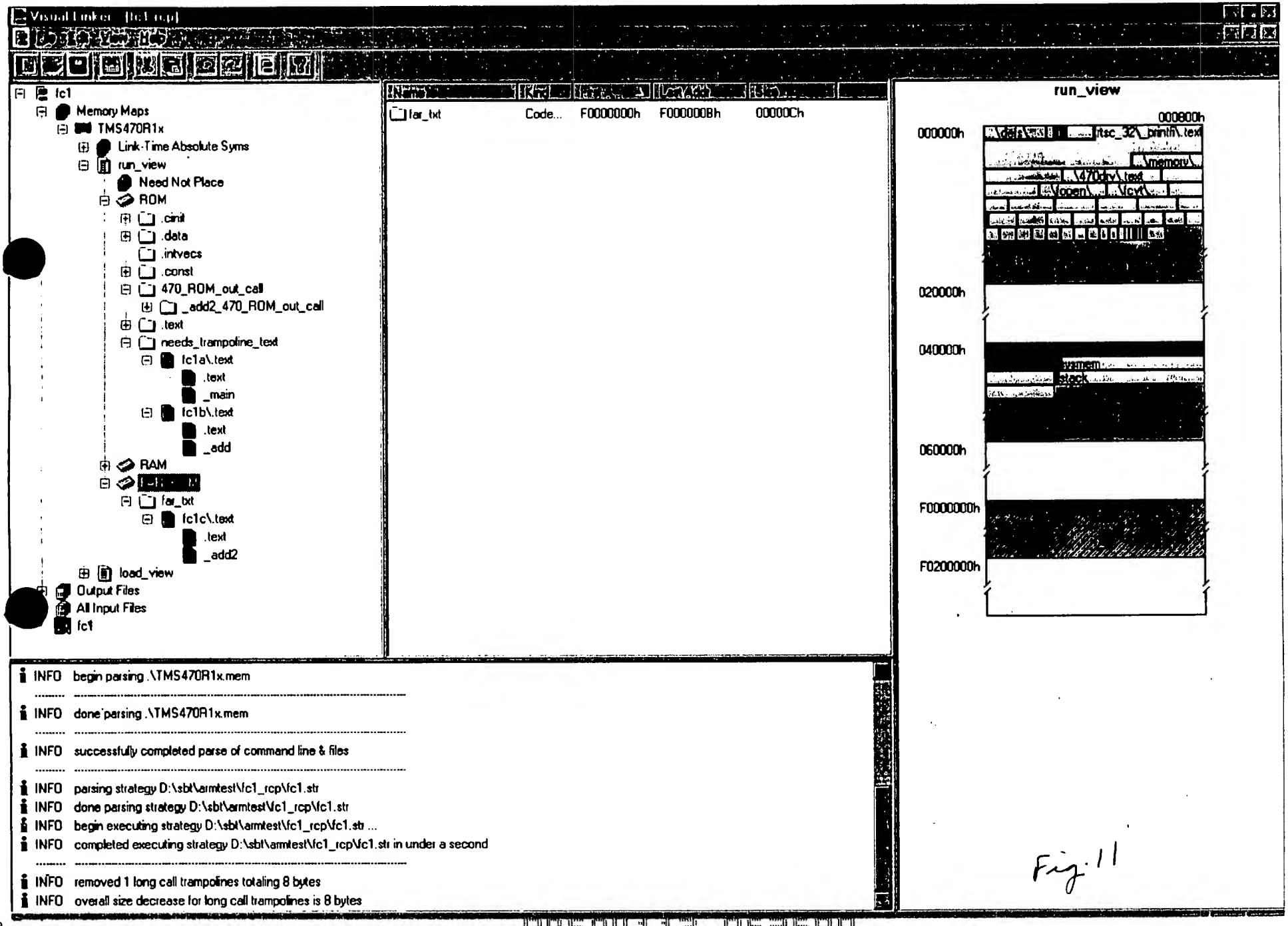

Fig. 11

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Fig. 12

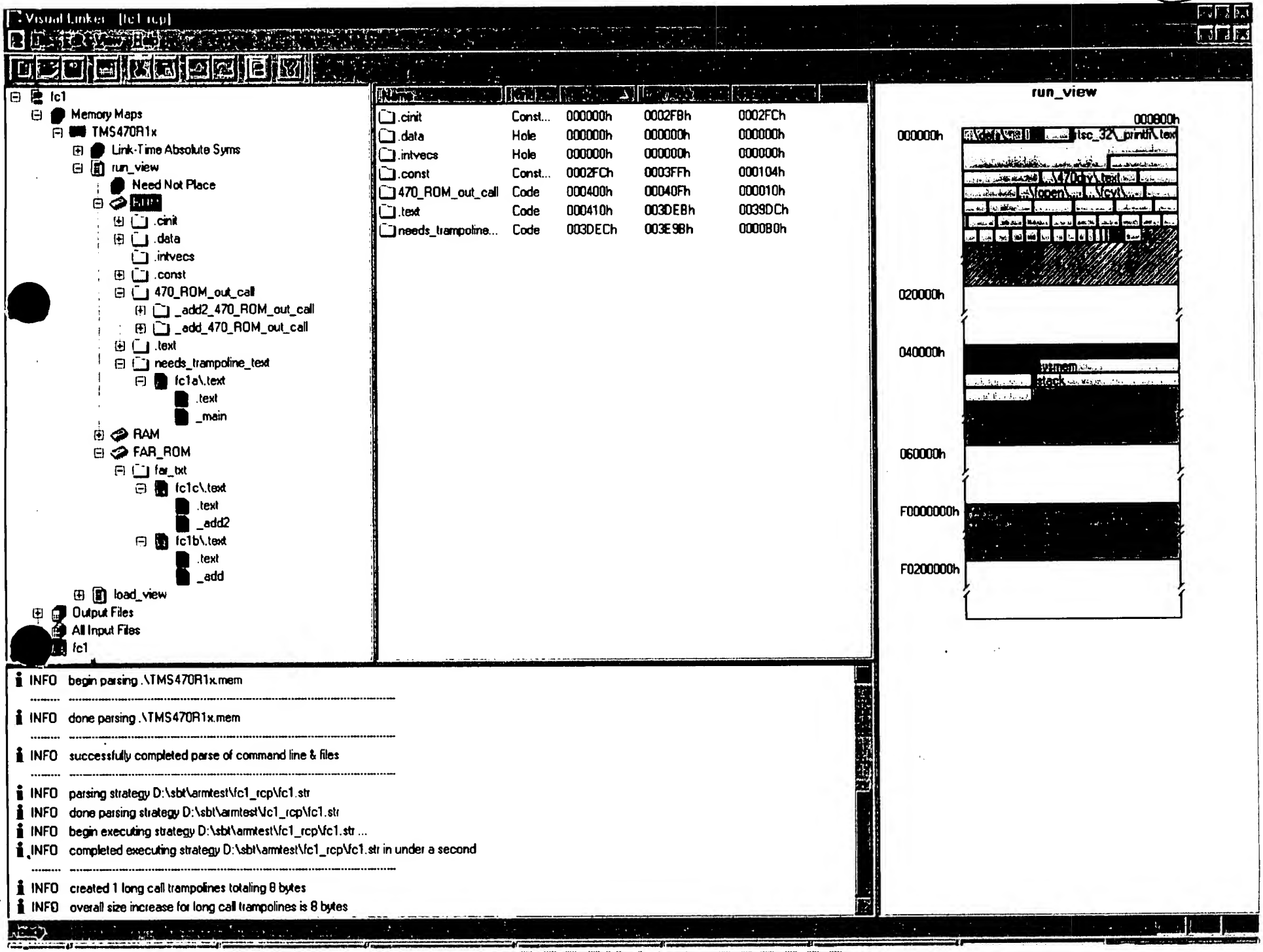

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Fig. 12